Dr. KAMLESH KUMAR SINGH

Ph.D. (Electronics), M.Tech.   **Senior Member IEEE**

Assistant Professor- III Dept. Of ECE   **Mob : +919451397255**

Amity University Uttar Prdesh Lucknow Campus   **E-mail: kksingh@lko.amity.edu**

Experience:

- **20+ Years** of teaching experience at **B.Tech.** Level, **18+ Years** of teaching experience at **M.Tech.** level and **9+ Years** of teaching at **Ph.D.** level.

- **Successfully Guided 02 Ph.D. Student.**

- **Guiding 04 Ph.D. Students** at ASET, Amity University, Lucknow.

- **Published 60+ research papers in International Journals.**

- **Presented 110+ papers in International and National Conferences, Seminars and Workshops.**

- **Published 04 Books.**

- **Published 10 Book Chapters.**

- **Guided about 40 Projects at B.Tech. Level and 15 Projects at M.Tech. Level..**

- **Senior Member IEEE, Member** of **IET-UK, ISEIS, IAENG**

- **Member Accreditation Committee University** for IET-UK, WASC, NACC, NIRF etc.

- **Proctor** at ASET, Amity University, Lucknow Campus.

- **Member of Advisory Council** at Meena Shah Institute of Technology and Management-Degree College, Gonda.

- **Convenor of IETE Zonal Seminar -2023.**

- **Convenor of IEEE SPS Seasonal School AMALGAM-2022.**

- **Convenor** of International Conference "**ACTSE-2021**".

- **Coordinator** of National Workshop On , "Solar Energy And Its Impact On Environment" in 2017

- **Organizing Chair of IEEE International Conference SPAR-2024** (To be conducted on 12th & 13th September, 2024)

Title of Ph.D. Thesis : TO DESIGN LOW VOLTAGE, WIDE BAND DIFFERENTIAL VOLTAGE CURRENT

CONVEYOR: A NOVEL APPROACH

Area of Specialization : VHDL Design, Image Processing, Internet Of Things and Internet of Everything

Academic Distinctions/Awards/Any other Relevant Information :

1.Editorial board Member of **SCOPUS** Journal and International Journals.

2.Member of Review Committee Of **SCI** and International Journals

3.Nodal Officer of IQAC- ASET, Amity University Lucknow Campus.

4.Branch Counsellor IEEE.

5. Best Paper awards in Conferences and Symposiums.

Projects Undertaken:

- **Implementation of DCT and IDCT on FPGA for Image Compression and Decompression**

  Language used **:** VHDL Tool **:** XILINX

  Duration   **:** 6 months, **Center for Electronics Design and Technology, Gorakhpur, India**

- **Fiber Optic Based Fire Alarm Systems**

  Environment **:** Hardware + Software (FMTAP)

  Duration   **:** 6 months, **Central Scientific Instruments Organization, Chandigarh, India**

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LUCKNOW, INDIA