

SHORT PROFILE

Dr. Senthilkumar Meyyappan obtained his B.Sc., M.Sc., M.Tech. and Ph.D. degrees in Electronics Engineering in the years 2002, 2004, 2006 and 2013 respectively. He has more than 15 years of academic and administrative experience in reputed Engineering Colleges. He has presented around 40 research papers in national and international conferences. He has published 24 research papers in reputed international journals in which a few of them are listed in Elsevier and Springer. He has 10 patents including 2 international patents and 1 international copyright to his credit. He has guided several UG and PG engineering students for their project work. He is the University 12th rank holder in M.Sc. Industrial Electronics. He received Rs. 1 Lakh as Grant-in-Aid from the Institution of Engineers (India) for the project titled “Development of Multi-Optimization Model for the Better Governance of Technical and Management Institutions through IT/ITES as an Integrated Approach” in the year 2015. He is the recipient of various awards such as Ariviyal Kalanjiyam (Eminent Scientist), Wall of Fame and Best Researcher given by various organizations. He is acting as reviewer, editorial board member and technical program committee (TPC) member for several reputed international journals and IEEE sponsored international conferences. He is the life member of Professional Societies such as Indian Society for Technical Education (ISTE), Computer Society of India (CSI) and Fellow of Institution of Electronics and Telecommunication Engineers (IETE). His areas of interest include Wireless Sensor Networks (WSN) and Outcomes Based Education (OBE). Currently, he is working as Associate Professor in the Department of Electronics and Communication Engineering, Nalla Malla Reddy Engineering College, Hyderabad, Telangana, India.

References (Complete Details Available Online)

1. <https://in.linkedin.com/in/dr-senthilkumar-meyyappan-1538844b>
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3. https://www.researchgate.net/profile/Senthilkumar_Meyyappan2